

2811
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/089,588

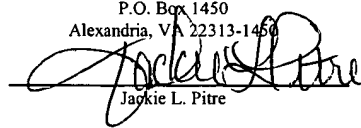
Confirmation No.: 2551

Filing Date: July 15, 2002

Inventors: Bois et al.

Title: SEMICONDUCTOR DEVICE
COMBINING THE ADVANTAGES
OF MASSIVE AND SOI
ARCHITECTURE, AND METHOD
FOR MAKING SAME

§ Examiner: Unknown
§ Art Unit: 2811
§ Atty. Dkt. No.: 5310-04500
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| CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8 |
| DATE OF DEPOSIT: <u>4/19/05</u> |
| I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail on the date indicated above and is addressed to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 |
|  Jackie L. Pitre |

INFORMATION DISCLOSURE STATEMENT

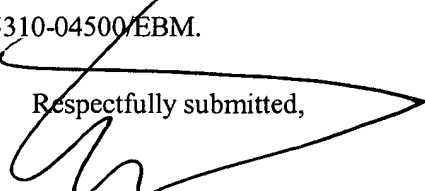
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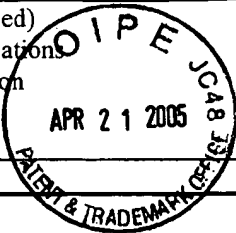
It is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 (US01-US26, F01-F12, and OA01-OA22) be considered by the Examiner and made of record. Copies of the listed references are enclosed for the convenience of the Examiner.

Should any fees be required, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 50-1505/5310-04500/EBM.

Respectfully submitted,


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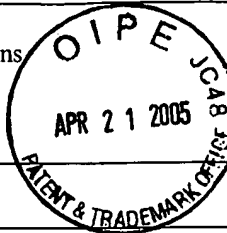
U.S. PATENT DOCUMENTS

| EXAM. INITIALS | REF. DES. | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
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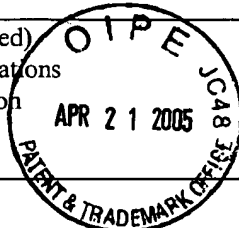
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OTHER ART

| EXAM. INITIALS | REF. DES. | OTHER ART (including Author, Title, Date, Pertinent Pages, etc.) |
|----------------|-----------|--|
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| | OA23 | International Search Report for PCT/FR 00/00642 mailed on July 3, 2000 (6 pages). |

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